### Course: C10073v7 BE BSci
#### Major: ICT - Computer Systems Engineering

#### Stage 1
- 33130 Maths Mod 1
- 33130 Core

#### Stage 2
- 48230 Eng Comm
- 48230 Core

#### Stage 3
- 48240 Design & Intro Software
- 48240 Embedded C

#### Stage 4
- 48510 Signal Theory
- 48510 Core

#### Stage 5
- 48430 Eng Eco & Fin
- 48430 Core

#### Stage 6
- 48310 Maths Mod 1
- 48310 Core

#### Stage 7
- 48510 EE1
- 48510 Core

#### Stage 8
- 48210 Interrogating
- 48210 Core

#### Stage 9
- 48471 ICT Analysis
- 48471 Core

#### Stage 10
- 48016 Capstone A
- 48016 Core

#### Stage 11
- 48026 EPR2 [C]
- 48026 Core

#### Stage 12
- 48100 Professional Practice
- 48100 Core

### Notes:
- [C] denotes an academic co-requisite, where this subject may be taken in the same semester.
- Check the UTS Timetable for availability.

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**Subject Name**

- **Subject Number**
- **Credit Points**
- **Academic Requisites**
- **Course Component Key**
- **Availability**

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**Subject Number**

- 48016 Capstone A
- 48026 EPR2 [C]
- MAJ

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**Availability**

- If blank, subject offered in either semester. Check the UTS Timetable.
## ICT Choice

Select 12 CP from the following:

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<tr>
<th>Course Code</th>
<th>Course Name</th>
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<td>48024</td>
<td>Applications</td>
</tr>
<tr>
<td>48023</td>
<td>Systems</td>
</tr>
<tr>
<td>48024</td>
<td>Embedded C</td>
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<tr>
<td>48024</td>
<td>Intro Digital Sys</td>
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<td>48520</td>
<td>Intro to Eng</td>
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<td>48024</td>
<td>Software Arch</td>
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<td>48440</td>
<td>Software Eng Pr</td>
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<td>48024</td>
<td>Applications Program</td>
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<td>48440</td>
<td>Signal Theory</td>
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<td>Mobile Comm</td>
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<td>48024</td>
<td>Networking 2</td>
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<td>48024</td>
<td>Software Eng Pr</td>
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<td>Digital Forens</td>
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<tr>
<td>48024</td>
<td>Networking Fundamentals</td>
</tr>
</tbody>
</table>

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**NOTES:**
- UTS Timetable remains the definitive source on subject availability.
- Your study plan defines course requirements.
- For further assistance, contact Student Centre at 1300 275 887.